

## Stravus Engineering Resources

Stravus has solid knowledge in the area of programmable logic (FPGAs & CPLDs) and VHDL. We have more than 5 years experience of the complete design environment for FPGAs. This includes all major device vendors as well as both front-end and back-end tools. We also have experience in educating others about design methodology.

Stravus has two main operating areas:

- **Design services**
- **Education**

One of our expressed objectives is to offer design services at compelling prices. Stravus is a small firm and as such, we do not suffer from a lot of overhead expenses. We are focusing 100% on our core business and this is why we can offer design services on time basis at **30-50%** lower prices than our established competitors.

Our second objective is to enable the world of FPGAs to small companies who does not necessarily have digital development as their main business but could still benefit from using it.

In order to achieve this we are developing training in fundamental FPGA design. The focus will be on using the free tools that are distributed by the device vendors but we will also explain the benefits of using high-end third party tools. These courses will be an excellent introduction to Programmable Logic for those who have none or limited experience in the area and will fit perfectly for companies who wish to evaluate FPGA synthesis. The courses will follow a new education method which makes it possible to offer them at extremely compelling prices.

We are also constantly evaluating existing tools in the lower price range to see if they are good enough for use in a real project.

Please visit our web for more information and contact details.



Michael Lundh

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