

## The Fundamentals of FPGA Design

Stravus has several years experience of educating engineers in the FPGA area. We have identified that the standard education approach more or less exclusively targets big companies or companies that have FPGA development as core business. It is often assumed that the customer has an extensive budget for FPGA training and tool investments. We believe that there are smaller customers who could really use training in the area but are not capable of large investments. Our goal is to provide relevant training for those potential users of FPGAs at as low costs as possible.

This course will teach you the complete design flow for a small to medium sized FPGA project. The focus will be on using the free tools that are distributed by the device vendors but we will also explain the benefits of using high-end third party tools. The course will be an excellent introduction to Programmable Logic for those who have none or limited experience in the area and will fit perfectly for companies who wish to evaluate FPGA synthesis. There are two different editions, Altera and Xilinx Edition. Each course will cover the following topics:

- Overview of EDA-tools and Design Flow
- FPGA Architecture and device specific features
- Synthesis
- Place&Route
- Introduction to simulation
- Timing
- VHDL (introduction + VHDL for testbenches)

The courses will follow a new education method which makes it possible to offer them at extremely compelling prices.

Please visit our web for more information and contact details.



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Stravus Engineering Resources

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